

Application Number:	10/583,052
Filing or 371 (c) Date:	06-14-2006
Application Type:	Utility
Examiner Name:	FAHERTY, COREY S
Group Art Unit:	2183
Confirmation Number:	8831
Attorney Docket Number:	NL03 1445 US1
Class / Subclass:	712/220
First Named Inventor:	Peter Dytrych . Leuven, (BE)

Title of Invention: Memory-efficient instruction processing scheme

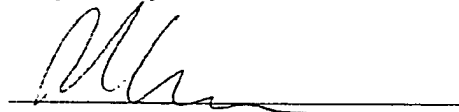
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESTRICTION RESPONSE

Sir:

Responsive to the Office Action of 12/14/2007, Applicant hereby elects Group I, claims 1-10, 12 and 13.

Respectfully submitted.



Michael J. Ure, Reg. 33,089

Dated: 2/14/2008